

**IN THE SPECIFICATION:**

The specification as amended below with replacement paragraphs shows added text with underlining and deleted text with ~~striketrough~~.

Please AMEND the specification beginning at page 18, line 15, as follows:

In order to realize the driving method in the sixth embodiment, the panel drive control portion 109 in the drive circuit of the PDP apparatus in FIG.1 is made to have a structure as shown in FIG.16 so that the period of the sustain pulse can be varied. In the panel drive control portion 109, a CPU121 controls the number of sustain pulses of each subfield according to the brightness adjust signal entered from the outside, the internal power control, and so on. The sustain period of each subfield is constant and the CPU 121 determines the period (frequency) of the sustain pulse based on the number of sustain pulses of each subfield and the length of the sustain period, generates the corresponding control data, and puts out to a D/A converter 122. The D/A converter 122 generates analog signals corresponding to the control data and applies it to a VCO 123. The VCO 123 generates clocks of a frequency corresponding to these analog signals (termed "an original clock frequency"), and supplies them to a scan driver control portion 110 and a common driver control portion 111. In this way, the clock period is varied.